IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
	)
Bartz et al.,	) Examiner: Whitmore, Stacy
	)
Serial No.: 09/989,808	)
	) Art Unit: 2825
	)
Filing Date: November 19, 2001	)
	)
FOR: AUTOMATIC GENERATION OF	)
APPLICATION PROGRAM INTERFACES,	)
SOURCE CODE, INTERRUPTS, AND	)
DATASHEETS FOR MICROCONTROLLER	)
PROGRAMMING	)
	)

DECLARATION UNDER 37 C.F.R. §1.132 TRAVERSING GROUNDS OF REJECTION UNDER 35 U.S.C. § 102(a)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I, Marat Zhaksilikov, do hereby declare and say:

My home address is 12825 67th Ave., SE, Snohomish, WA 98296.

On November 16, 2001 I signed a declaration stating that I believe

that I am an original and joint inventor of the subject matter that

is claimed in U.S. Patent Application No. 09/989,808, entitled,

"Automatic Generation Of Application Program Interfaces, Source

Code, Interrupts, And Datasheets For Microcontroller Programming"

(hereinafter, the present Patent Application). The present Patent Application was filed in the United States Patent and Trademark Office on November 19, 2001. The declaration stating that I believe that I am an original and joint inventor of the subject matter claimed in the present Patent Application was filed in the United States Patent and Trademark Office, along with the present Patent Application.

I have studied the entire specification, drawings, and claims of the present Patent Application. I understand the language of the claims and the scope thereof. I understand the invention as claimed.

As an employee of Cypress Microsystems, a subsidiary of Cypress Semiconductor, subject matter that I originate may be incorporated into Cypress documents, some of which may become public.

I have reviewed Revision 1.0 of the document "PSoC Designer: Integrated Development Environment: Getting Started 12-Minute Tutorial" (hereinafter, IDE Tutorial). It is my belief that, to the extent that subject matter exists in the document "IDE Tutorial" that may anticipate claims of the present Patent Application, such information was derived from the work of myself and my co-inventors of the present Patent Application: Manfred Bartz, Steve Roe, Kenneth Y. Ogami, Matthew A. Pleis, and Douglas H. Anderson.

I have reviewed "PSoC™ Technology Completely Changes 8-Bit MCU

System Design!" (hereinafter, PSoC™ Technology). It is my belief

that, to the extent that subject matter exists in the document

"PSoC™ Technology" that may anticipate claims of the present Patent

Application, such information was derived from the work of myself

and my co-inventors of the present Patent Application: Manfred

Bartz, Steve Roe, Kenneth Y. Ogami, Matthew A. Pleis, and Douglas H.

Anderson.

I further declare that all statements made herein of my own

knowledge are true and that all statements made on information

and belief are believed to be true; and further that these

statements were made with the knowledge that willful false

statements and the like so made are punishable by fine or

imprisonment, or both, under Section 1001 of Title 18 of the

United States Code, and that such willful false statements may

jeopardize the validity of the application or any patent issued

thereon.

                        Respectfully submitted,

Date: _July 25_, 2005   _____
                                   Signature

                        Full Name: Marat Zhaksilikov